

596423	Class	Subclass	ISSUE CLASSIFICATION

UTILITY SERIAL NUMBER	PATENT DATE	PATENT NUMBER
596423		

SERIAL NUMBER	FILING DATE	CLASS	SUBCLASS	GROUP ART UNIT	EXAMINER
08/596,423	02/02/96	327	534	2504	Cunning

APPLICANTS
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CONTINUING DATA***
VERIFIED THIS APPLN IS A CON OF 08/376,347 01/23/95 ABN
TDC

FOREIGN/PCT APPLICATIONS***
VERIFIED REPUBLIC OF KOREA 1994-1124 01/21/94
TDC

Foreign priority claimed 35 USC 119 conditions met	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no	AS FILED	STATE OR COUNTRY	SHEETS DRWGS.	TOTAL CLAIMS	INDEP. CLAIMS	FILING FEE RECEIVED	ATTORNEY'S DOCKET NO.
Verified and Acknowledged	Examiner's Initials	→	KRX	3	6	3	\$750.00	220312

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TITLE
SUBSTRATE BIAS GENERATOR IN SEMICONDUCTOR MEMORY DEVICE

U.S. DEPT. OF COMM./PAT. & TM—PTO-436L

PARTS OF APPLICATION
FILED SEPARATELY

NOTICE OF ALLOWANCE MAILED

Amount Due	Date Paid	Assistant Examiner	Total Claims	Print Claim

Amount Due	Date Paid	Primary Examiner	Sheets Drwg.	File Drwg.	Print Fig.

Label Area	ISSUE BATCH NUMBER

PREPARED FOR ISSUE

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PTO-436A
Rev. 8/92

(FACE)

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